4. Pack the two unpacked BCD numbers stored in memory locations 4200H and 4201H and store result in memory location 4300H. Assume the least significant digit is stored at 4200H.

Program:

# PACK AND UNPACK

LDA 4200H

MOV B, A

LDA 4201H

RRC

RRC

RRC

RRC

ORA B

STA 4300H

HLT

5. Two digit BCD number is stored in memory location 4200H. Unpack the BCD number and store the two digits in memory locations 4300H and 4301H such that memory location 4300H will have lower BCD digit.

LDA 4200H

MVI B, 0FH

ANA B

STA 4201H

LDA 4200H

MVI B, F0H

ANA B

RRC

RRC

RRC

RRC

STA 4202H

HLT

6. Write a set of instructions to alter the contents of flag register in 8085.

7. Calculate the sum of series of numbers. The length of the series is in memory location 2200H and the series begins from memory location 2201H.

a) Consider the sum to be 8 bit number. So, ignore carries. Store the sum at memory location 2300H.

b) Consider the sum to be 16 bit number. Store the sum at memory locations 2300H and 2301H.